

**AMENDMENTS TO THE ABSTRACT**

Please substitute the following paragraph for the abstract now appearing in the currently filed specification:

An MPEG video decoding system including: a buffer for storing an MPEG-formatted or a DV-formatted video signal; a VLD/IQ unit for performing a variable length decoding and an inverse quantization of the video signal; an IDCT device for selectively performing an 8×8 IDCT and a 4×8 IDCT according to the format of the inversely quantized signal; an adder for bypassing and storing the output signal of the IDCT unit into an external memory if the output signal of the IDCT unit is an MPEG-formatted I-picture or a DV format, and for adding the IDCT-ed signal and a motion compensated signal if the output signal of the IDCT device is an MPEG-formatted P-picture or B-picture; and a motion compensator for performing a motion compensation outputting the motion compensated signal to the adder, if the output signal of the IDCT device is the MPEG-formatted P-picture or the MPEG-formatted B-picture.